Diagram of gates

In digital electronic logic gates a physical device to implement the boolean function. It able to perform a logical operation, Take one or more input and produce a single binary output signal. Logical gates are implemented by Diode, Transistor, Semiconductor devices, etc.

**Not**/**Complement**/**Negation**

This gate always take only one input and return opposite output.

**x=true** then **~x=false**

**x=false** then **~x=true**

**x x**

**Or**/**Disjunction**

This gate can two or more than two input, So this gate not a binary gate, If any one is true then output will be true.

T + F + 0 = T

F + F + F = F

T + T + T = T

**And**/**Conjunction**

This gate can two or more than two input, So this gate not a binary gate, If all inputes are true then output will be true.

T + T + T = T

T + F + F = F